ELE4112 MICROPROCESSOR BASED SYSTEMS

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<th>Hours per Semester</th>
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<td>Total Mark</td>
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Rationale
This course introduces microprocessor architecture and discusses the design of systems based on microprocessors and microcontrollers.

Course Objectives
• To provide students with an understanding of microprocessor based systems and their use in instrumentation, control and communication systems
• To Investigate microprocessor based systems, produce software for a microprocessor based system, interface microprocessor based systems and understand usage of programmable logic controllers

Detailed Course Content:
Microprocessor-based systems: [16 Hours]
Historical and technological background, Types of microprocessor: speed of processing and data transfer, cost, i/o facilities, physical size; types drawn from 8,16,32 bit systems, single chip/microcontroller and multi chip systems, dedicated/embedded and PC/workstation systems, CISC and RISC processors, Intel and Motorola types. Applications: control systems (e.g. car engine management, robotics, distributed control systems, printers); instrumentation systems (e.g. data acquisition and logging systems, indicator display systems, ‘intelligent’ panel instruments); communication systems (e.g. facsimile machines, modems, radio transmitters, radar systems); commercial systems (e.g. eftpos systems, electronic bank teller machines, hand held stock loggers, PCs)

Software for a microprocessor-based systems (micro programming): [18 Hours]
Design software: algorithms in the form of a structure chart showing actions and conditions or in pseudo code (structured English), in sufficient detail to allow coding to proceed. Specification: the specifications should be sufficiently demanding to require modularization and the passing of data between modules. Specifications should focus on the use of microprocessor based system in applications requiring interfacing to devices such as lights, switches, motors, heaters, dumb terminals, keypads, LCD and LED displays, printers, ADCs and DACs and other computer systems.

Interfacing: [11 Hours]
Interfacing techniques; Timing and synchronization interfaces; IC peripheral chips; Programmable parallel interface: devices in this category include M68230 PIT, 8255 PPI, 6522 VIA and Z80PIO. Interrupts should also be considered. Programmable serial interface: devices in this category include M68681 DUART, 8250 UART, M6850 ACIA. Serial interface standards (RS 232 and RS 422/RS 423); Memories and interface circuits; Direct Memory Access; Sliced processors. Instructions and addressing modes.; Operating Systems Compilers, and programming considerations.

Learning Outcomes
Having successfully completed the module, you will be able to:
• demonstrate knowledge and understanding of digital techniques and hardware design principles necessary to underpin your education in your chosen engineering discipline and apply them to the design of modern electronic systems.
• analyse and design logic circuits using programmable logic and will develop a basic understanding of hardware descriptions in software.
• design, understand and implement combinational and sequential logic designs using
programmable logic devices,
• develop hardware descriptions in SystemVerilog and use them in digital design applications.
• combine theory and experience in developing new concepts and creatively apply them in new designs.

**Method of Teaching / Delivery**
The course will be taught by using lectures, tutorials and assignments.

**Mode of Assessment**
Assignments, tests and final examination. Their relative contributions to the final grade are:

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<th>Requirement</th>
<th>Percentage Contribution</th>
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<tr>
<td>Course work (Assignments, tests)</td>
<td>40%</td>
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<td>% Final examination</td>
<td>60%</td>
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<td>% Total</td>
<td>100%</td>
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**Recommended and Reference Books**

**Possible Lecturers:**
- Dr. J. Butime
- Mr. D. Nsubuga Mubiru
- Mr. P. Bogere
- Mr. G. Bakkabulindi
- Dr. Ing. L. L. Kaluuba